

CLIPPEDIMAGE= JP363268394A

PAT-NO: JP363268394A

DOCUMENT-IDENTIFIER: JP 63268394 A

TITLE: SIGNAL AMPLIFIER

PUBN-DATE: November 7, 1988

INVENTOR-INFORMATION:

NAME

NAKAGAWA, YUJI

ASSIGNEE-INFORMATION:

NAME

MATSUSHITA ELECTRIC WORKS LTD

COUNTRY

N/A

APPL-NO: JP62102655

APPL-DATE: April 24, 1987

INT-CL (IPC): H04Q009/00;H03F003/62 ;H04B003/00

US-CL-CURRENT: 379/90.01,379/395

ABSTRACT:

PURPOSE: To prevent a state impossible to receive a signal due to the lowering of impedance on a reception side from being generated, by providing two transformers which separate the signal at every positive and negative sign when the transformer is designated as the input side of the signal, and synthesize positive and negative signals when it is designated as the output side of the signal.

CONSTITUTION: The transformers T<SB>1</SB> and T<SB>2</SB> function in such a way that they separate the signal consisting of pulses with positive and negative polarity at every positive and negative sign when they are designated as the input side of the signal, and they synthesize the positive and the negative signals when they are designated as the output side of the signal. Transistors 6<SB>1</SB>∼6<SB>4</SB> constitute a reception part receiving via the transformers T<SB>1</SB> and T<SB>2</SB> at every positive and negative sign. Also, transistors 8<SB>1</SB>∼8<SB>4</SB> constitute a transmission part which synthesizes and transmits the signals of positive and negative polarity at the transformer T. In such a way, it is possible to perform satisfactory signal transmission even when plural terminal equipments 2 are connected to a home bus (1), or long distance transmission is performed.

COPYRIGHT: (C)1988,JPO&Japio

CLIPPEDIMAGE= JP406284118A

PAT-NO: JP406284118A

DOCUMENT-IDENTIFIER: JP 06284118 A

TITLE: DIGITAL SIGNAL RECEPTION CIRCUIT

PUBN-DATE: October 7, 1994

INVENTOR-INFORMATION:

NAME

ONO, MASAHIKO

ASSIGNEE-INFORMATION:

NAME

NEC CORP

COUNTRY

N/A

APPL-NO: JP05065540

APPL-DATE: March 24, 1993

INT-CL (IPC): H04L007/027;H04L025/03

ABSTRACT:

PURPOSE: To extend the transmission distance by suppressing jitter of a detection signal generated due to waveform distortion of a cable so as to enhance the stability of a clock extract circuit.

CONSTITUTION: After a reception signal is subjected to equalization amplification by an equalization amplifier 1, the amplified signal is waveform-shaped and eye-pattern locus is made narrow by a pulse sharpner 3, a 1st comparator 7 and a 2nd comparator 8 decide a code and a clock extract device 6 is driver via a logic circuit 9. Jitter of a pulse width of a detection signal is suppressed by sharpening the pulse thereby allowing the clock extract device 6 to be operated stably against code interference caused in a long distance cable.

COPYRIGHT: (C)1994,JPO

CLIPPEDIMAGE= JP407075088A

PAT-NO: JP407075088A

DOCUMENT-IDENTIFIER: JP 07075088 A

TITLE: TELEVISION INTERCOM DEVICE

PUBN-DATE: March 17, 1995

INVENTOR-INFORMATION:

NAME

NAGAYAMA, MUNEHITO

IMAI, YUKIO

ASSIGNEE-INFORMATION:

NAME

AIPHONE CO LTD

COUNTRY

N/A

APPL-NO: JP05216607

APPL-DATE: August 31, 1993

INT-CL (IPC): H04N007/18;H04M009/00

ABSTRACT:

PURPOSE: To make a picture quality monitored by a television monitor clear even when the distance of two-line transmission lines is long, to boost the decreased voltage of a direct current power supply for a camera, and to make the operation of a camera stable by providing a voltage boost circuit and an amplifier for correcting a video-signal between the indoor master device side terminal and indoor master device of the two-line transmission lines.

CONSTITUTION: When the direct current power supply for operation is supplied, a supply voltage detecting circuit 12 of a transmission line adaptor AQ<SB>1</SB> is operated, and the voltage decreased by two-line transmission lines CBL<SB>1</SB> and CBL<SB>2</SB> is boosted by a voltage boost circuit 13 provided between a slave device (-) side terminal T<SB>2</SB> and a master device (-) side terminal T<SB>4</SB>. Also, when a front door slave device SQ<SB>1</SB> with a camera is operated, and an FM video signal is transmitted from the front door slave device SQ<SB>1</SB> with a camera to the two-line transmission lines CBL<SB>1</SB> and CBL<SB>2</SB>, an amplifier 11 for correcting a video signal of the transmission line adaptor AQ<SB>1</SB> corrects the high pass components of the FM video signal, obtains a flat characteristic similar to the FM video signal outputted from the front door slave device SQ<SB>1</SB> with a camera, and transmits the FM video signal to an indoor master device MQ<SB>1</SB>.

COPYRIGHT: (C)1995,JPO